In the Specification:

Please change the paragraph beginning on page 7, line 19 as follows:

~~Figure 12 is~~ Figures 12A and 12B are a functional block diagram of an example of an information processing subsystem for the combined switch and service processor module of Figure 6;

Please change the paragraph beginning on page 8, line 13 as follows:

Figure 19 is a schematic representation of aspects of the embodiments of Figures 11 and ~~12~~ 12A-B for use in explaining an embodiment of the present invention;

Please change the paragraph beginning on page 43, line 9 as follows:

With reference to ~~Figure 12~~ Figures 12A and 12B, there now follows a description of an example of a combined switch and service processor (CSSP) 71. In the present example, each CSSP 71 provides the functionality of a Switch 73 and of a Shelf Service Processor, or Shelf Service Processor (SSP) 74.

Please change the paragraph beginning on page 43, line 14 as follows:

~~Figure 12 provides~~ Figures 12A and 12B provide an overview of the functional components of the CSSP 71 including functional components of the Switch 73 and functional components of the SSP 74. In the present example, most of the components relating to the Switch 73 are mounted on a Switch PCB 231, and the components relating to the SSP 75 are provided on a SSP PCB 232. However, it should be noted that the components located in the lower portion of the switch PCB 321 (i.e., that portion below the SSP PCB 232 as illustrated in ~~Figure 12~~ Figures 12A and 12B logically belong to the SSP 74, rather than to the switch 73. It will be appreciated that such component arrangements are not compulsory for successful

operation and that any other component arrangement over any number of component boards can be easily achieved using conventional component arrangement techniques.

Please change the paragraph beginning on page 43, line 26 as follows:

Firstly, with reference to ~~Figure 12~~ Figures 12A and 12B, there follows a description of functional elements of the Switch portions 73 of a CSSP 71 as contained within the CSSP enclosure 121.

Please change the paragraph beginning on page 46, line 20 as follows:

With continued reference to ~~Figure 12~~ Figures 12A and 12B, there now follows a description of functional elements of the Shelf Service Processor (SSP) portion 74 of a CSSP 71 as contained within the CSSP enclosure 121 and provided on an SSP PCB 232.

Please change the paragraph beginning on page 56, line 1 as follows:

In the present example, the connections by means of which control and configuration of the shelf 41 are performed are entirely separate to the connections to the core data network 330. Therefore, a first external switch 335 can connect to a management (I2C) port 273 of the first CSSP 71 and a second external switch 336 can connect to a management (I2C) port 273 of the second CSSP 72. As described above with reference to ~~Figure 12~~ Figures 12A and 12B are, the management port 273 can provide a management network interface to both the switch 73 and SSP 74 of each CSSP 71. The external switches 335, 336 can each be connected to each of a pair of System Management Server (SMSs) 338, 339. The SMS is not essential to the operation of the shelf 41, but use thereof aids optimal operation of the shelf 41. In a typical multiprocessor server system a plurality of shelves 41 may be connected together via the core data network 330 under the control of a single management network utilising one set of SMSs 338, 339. A set of SMSs 338, 339 may comprise a single SMS (as well as a plurality thereof). However use of at least two SMSs enables redundancy of components, therefore increasing overall system reliability.